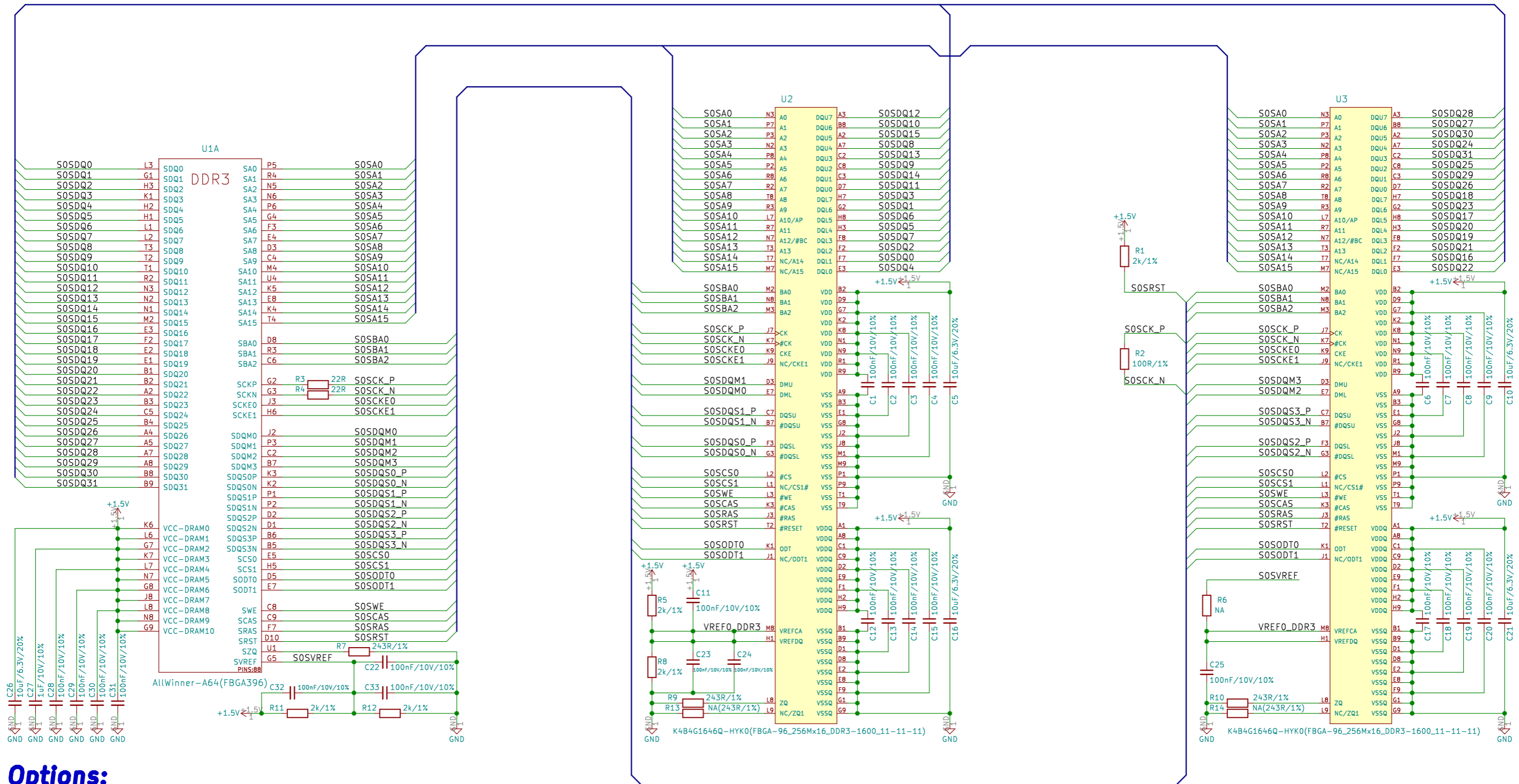


DDR3:1GByte



Options:

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1. Use 2(DDR3 256Mx16 Memory chips)x4Gb = 1GByte, i.e. 2xH5TQ4G63MFR-PBC(or K4B4G1646Q-HYK0) -> Default
2. Use 2(DDR3 512Mx16 Memory chips)x8Gb = 2GBytes, i.e. 2xH5TC8G63AMR-PBA(or K4B8G1646Q-MYK0)

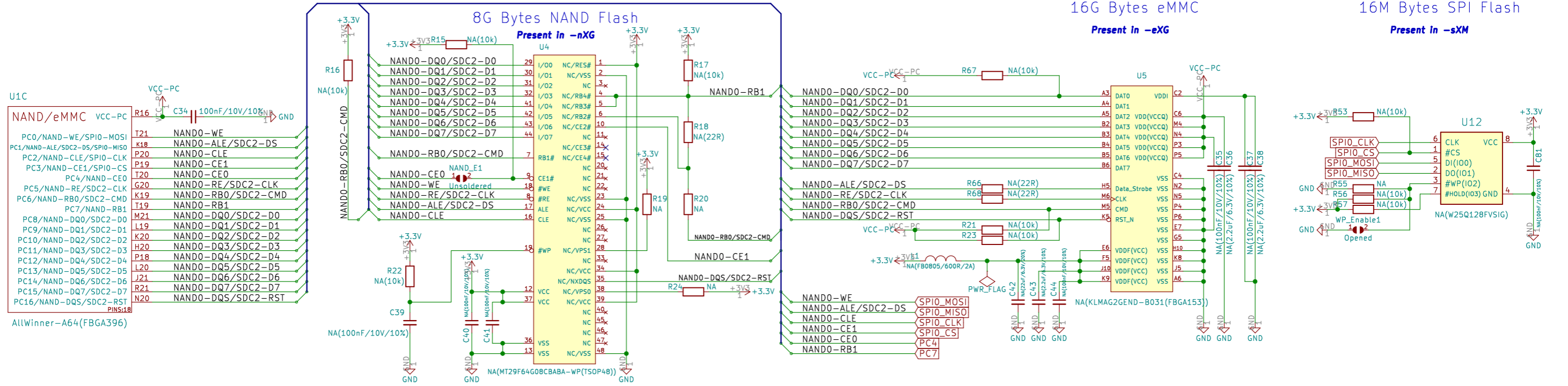
Note:

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We have used a number of fully compatible, but different DDR3 memories due to supply unavailability. In such cases the memory part name in the schematic might remain outdated. It is recommended to always refer to the exact memory name printed on the component itself.

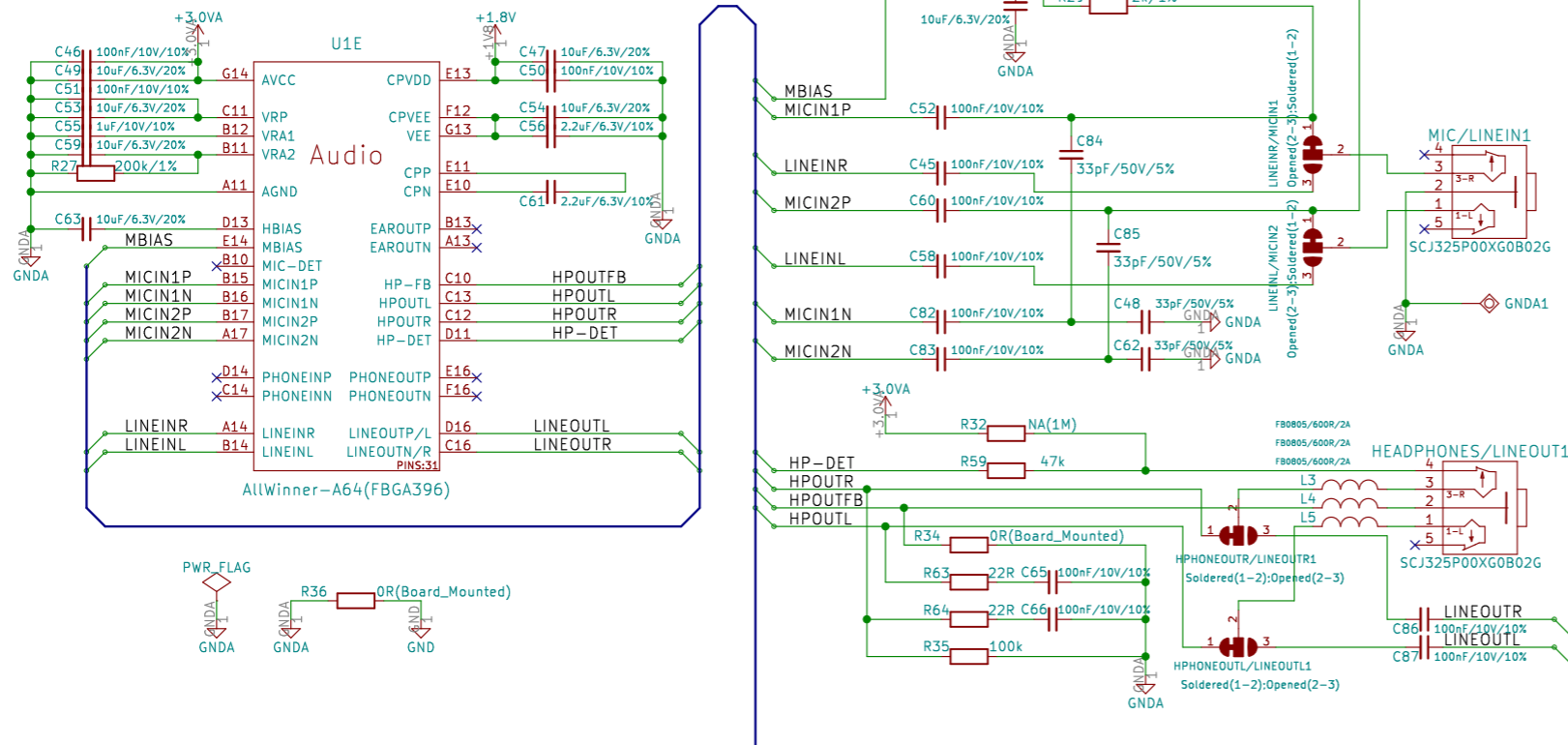
NAND Flash

Option!
 =====
 eMMC is default, because in this case, SPI0 is not multiplexed with eMMC as is with the NAND Flash!
 SPI0 is available on the UEXT connector!

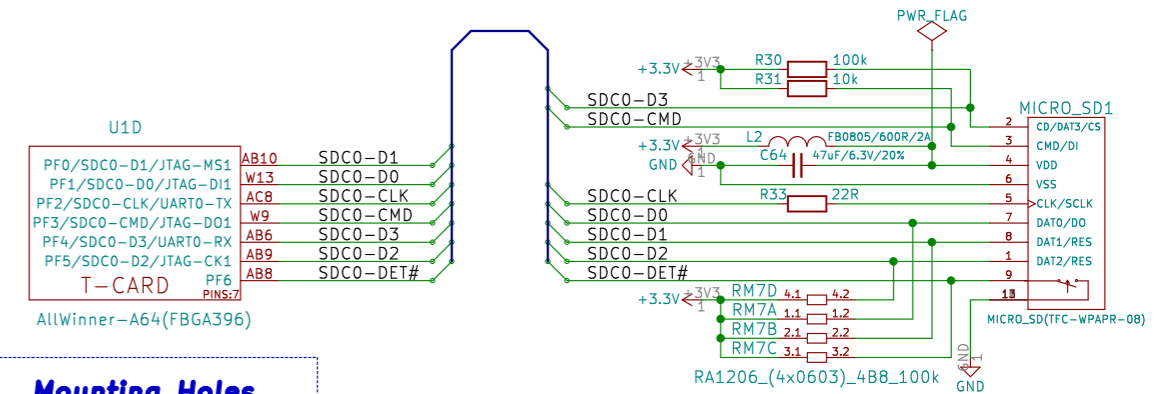


Audio

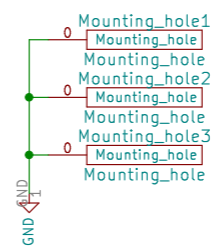
CPVEE and VEE to be made as Kelvin connection.



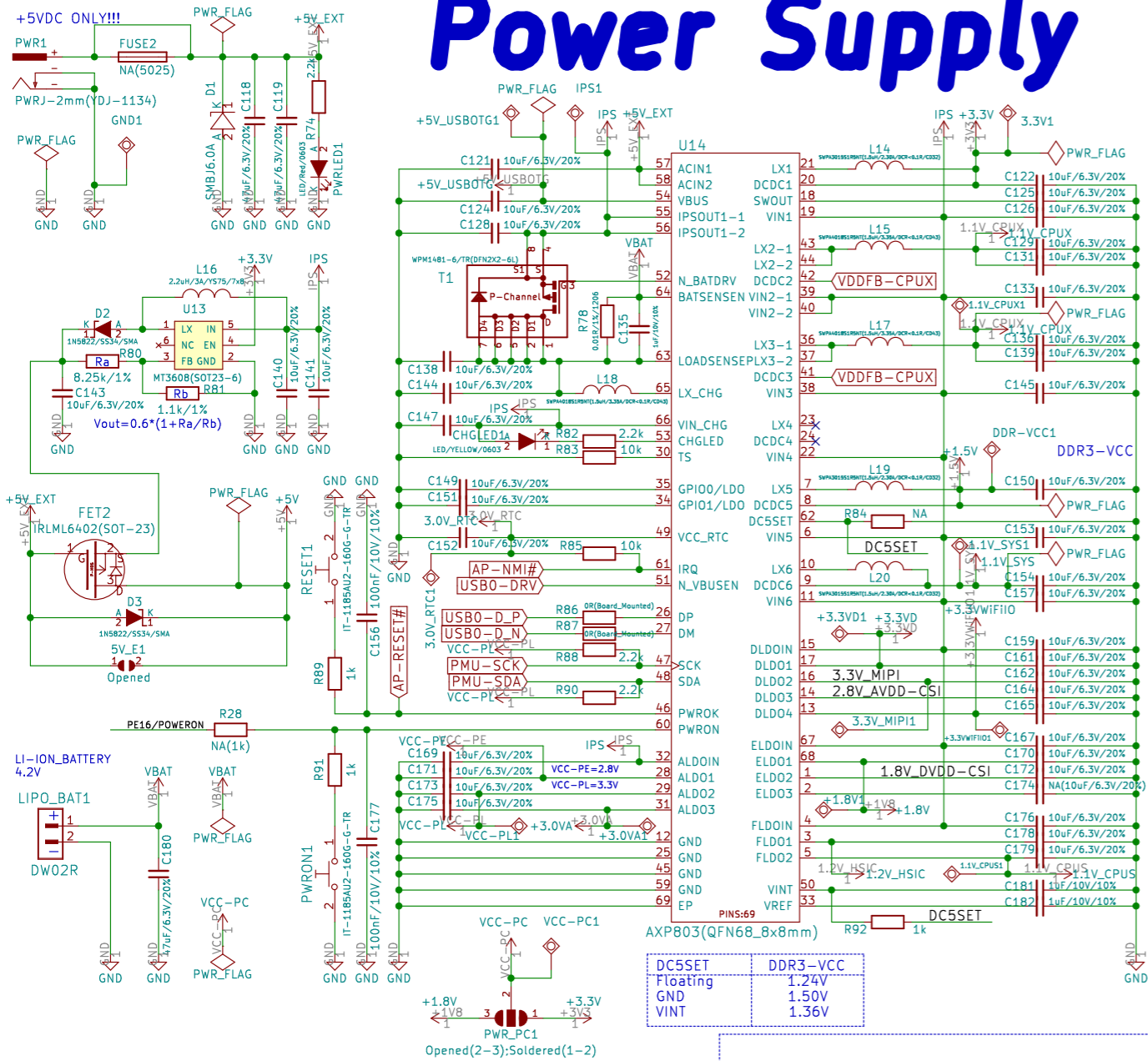
T-Card



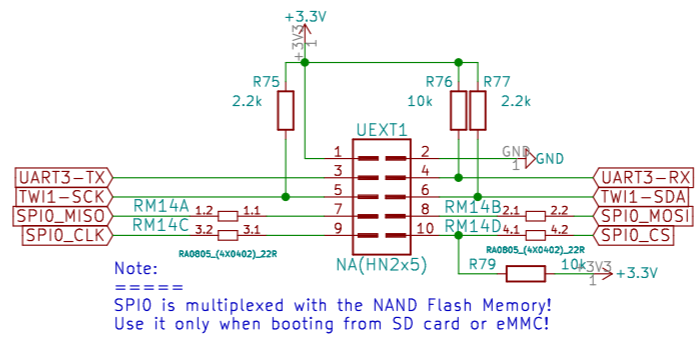
Mounting Holes



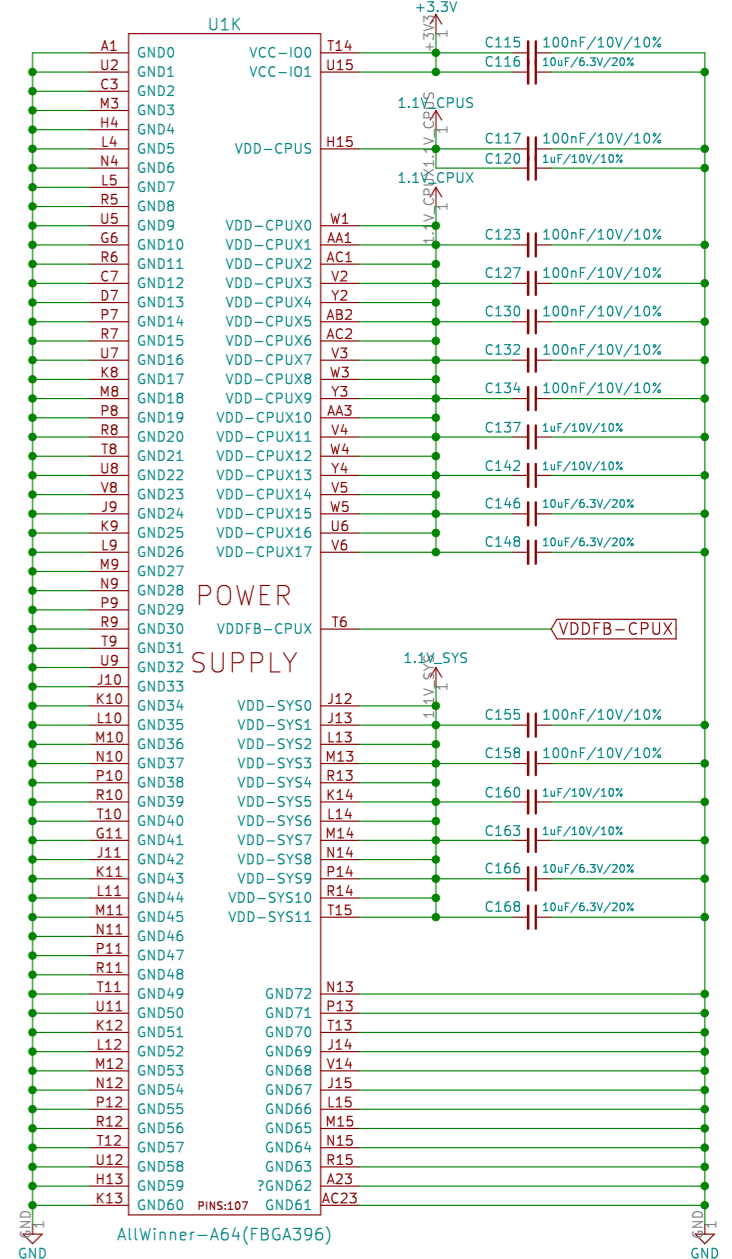
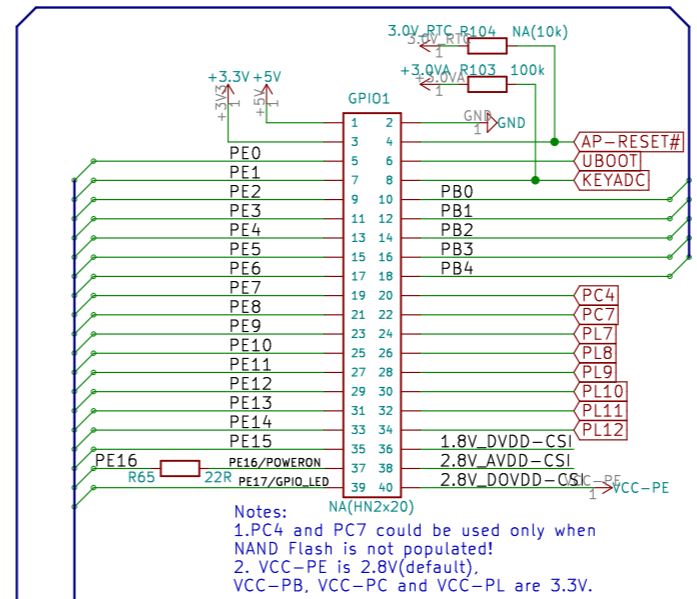
Power Supply



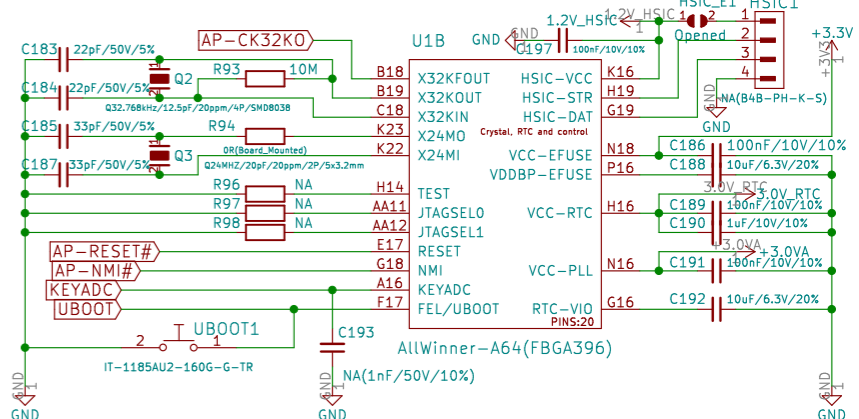
UEXT



Extension



Crystal, RTC & Control



MiPi-DSI

